



Klipper Building Options for BTT SKR MINI E3 V2.0:

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Klipper Firmware Configuration
[*] Enable extra low-level configuration options
Micro-controller Architecture (STMicroelectronics STM32)
Processor model (STM32F103)
Bootloader offset (28KiB bootloader)
Clock Reference (8 MHz crystal)
Communication interface (USB (on PA11/PA12))
USB ids
[ ] Specify a custom step pulse duration
(!PA14) GPIO pins to set at micro-controller startup

*1 select "Enable extra low-level configuration options" and configure "GPIO pins to set at micro-controller startup" to "!PA14".

[Space/Enter] Toggle/enter    [?] Help    [/] Search
[Q] Quit (prompts for save)    [ESC] Leave menu

```

The "make flash" command does not work on the SKR mini E3. Instead, after running "make", copy the generated "out/klipper.bin" file to a file named "firmware.bin" on an SD card and then restart the SKR mini E3 with that SD card.

Marlin 2.0.x Firmware Changes:

In Platformio.ini file change: `default_envs = STM32F103RC_btt_maple`

In Configuration.h file change:

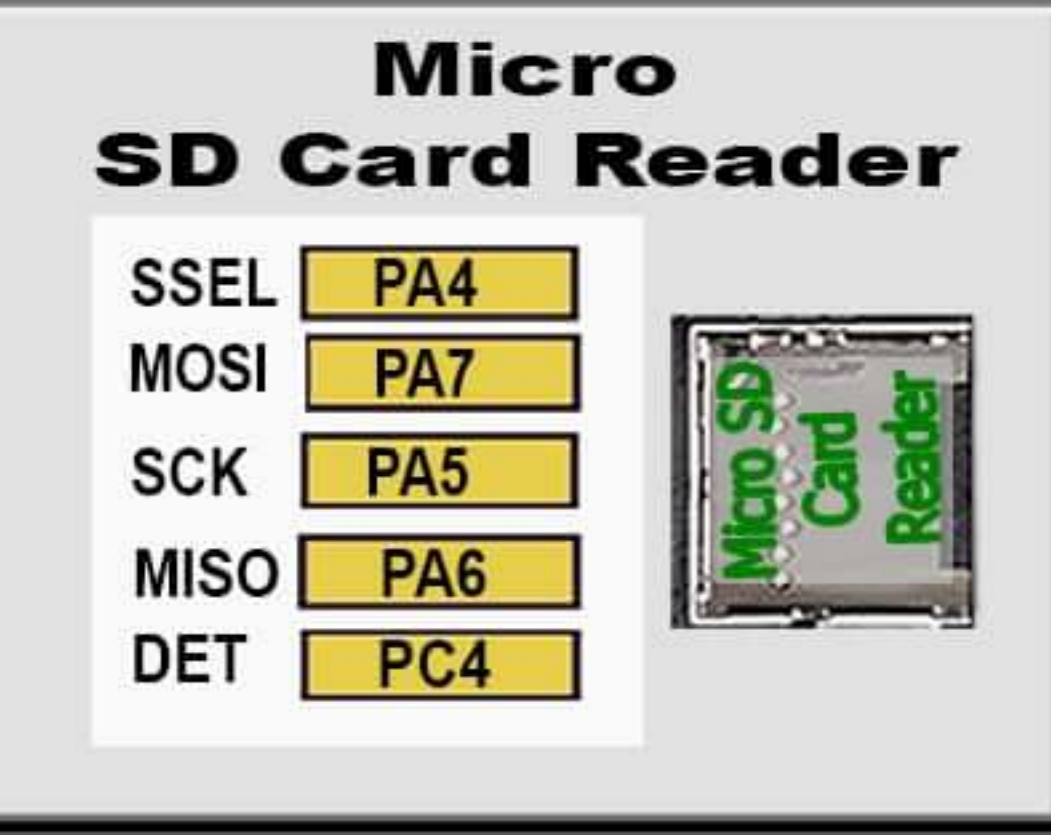
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#define SERIAL_PORT -1
#define SERIAL_PORT_2 2
#define MOTHERBOARD BOARD_BTT_SKR_MINI_E3_V2_0

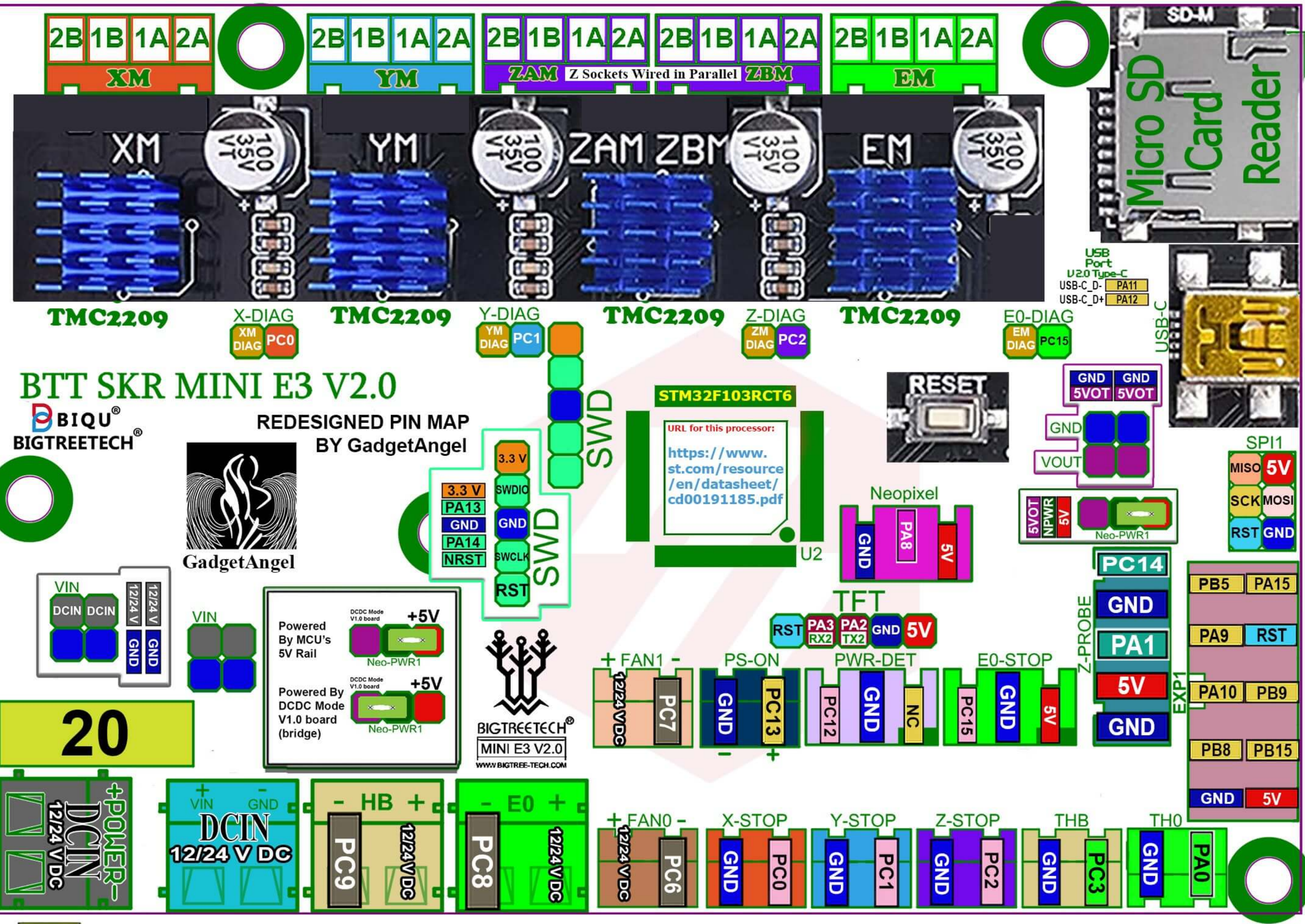
```

Note: Serial Port definitions in Marlin 2.0.x for this Board:

-1: USB Port; 2: TFT Port;



Note: If you are unsure about any of the information provided on this PIN Diagram, please ask for help from the 3D printer community, check the Processor's data sheet and board's schematic diagram.



EN	STEP	DIR	
XM	PB14	PB13	PB12
YM	PB11	PB10	PB2
ZM	PB1	PB0	PC5
EM	PD2	PB3	PB4

MOTOR UART

RX	PC11
TX	PC10

STALLGUARD (Sensor-less Homing)

DIAG PIN	ENDSTOP
XM	X-DIAG PC0 X-STOP
YM	Y-DIAG PC1 Y-STOP
ZM	Z-DIAG PC2 Z-STOP
EM	E0-DIAG PC15 E0-STOP

20 For the heated bed, logic, fans and hotend

